HM-APL-PCB
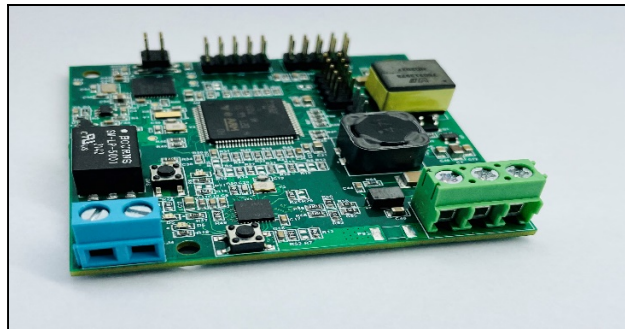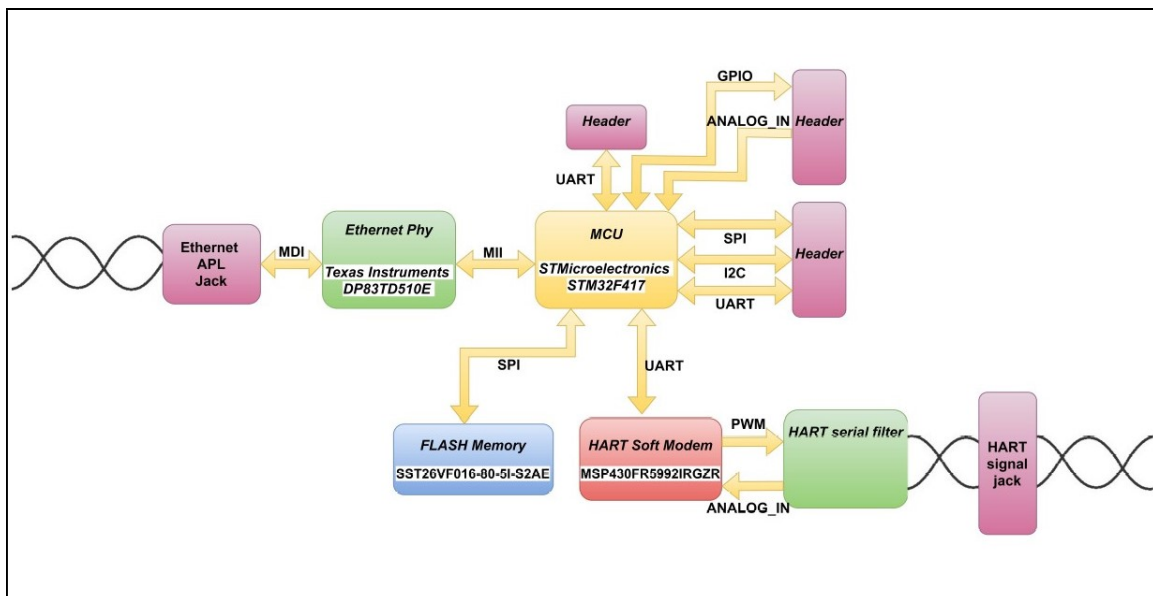### HART to Ethernet-APL Adapter PCB

Description:

The PCB converts existing HART transmitters to the new Ethernet Advanced Physical Layer (APL) interface. The PCB offers an easy migration path for existing HART Transmitters. The PCB can be modified to incorporate custom features or size requirements.

PCB Showing Connector Interface

Block Diagram

Features and Benefits:

- Easily convert existing HART Transmitters to Ethernet-APL
- Supports projects to design "direct to APL" transmitters without HART
- Works with SLAA Class APL devices
- APL connection supplies power so no external power supply needed
- HART-IP protocol support included
- Small Form factor, 2.5" x 2.5" (63mm x 63mm)
- Low power consumption for integration into Hazardous Area designs
- External Analog I/O for connections to external sensors or other peripherals
- I2C and SPI flash available for device logs, DD file, FDI package, and other additional data
- Secure firmware updates for easy development updates
- Complete documentation and support

Specifications:

Dimensions	2.5" x 2.5" (63mm x 63mm)
Total Unit Weight	6 oz (170 g)
Temperature	-40C to 85C
HART Specifications	HART 4, HART 5, HART 6, HART 7, HART Physical Layer Spec HCF_SPEC-54
APL Specifications	Ethernet-APL Power Test Specification v1.1 HCF_TEST-008 FCG TT20008 v1.0
Maximum Power Draw	540mW
Power for peripherals	135mW
Memory	512KB Flash

Components:

APL Interface	Texas Instruments DP83TD510E
Microcontroller	STM32F417 ARM Cortex M4

ProComSol, Ltd

Process Communications Solutions

Phone: 216.221.1550
Toll Free: 877.221.1551
Email: sales@procomsol.com
Web: www.procomsol.com